



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 09/019,087 Confirmation No.: 1640
Applicant : Hiroyuki Enomoto
Filed : 02/05/1998
TC/A.U : 1765
Examiner : Kunemund, Robert M.
Docket No. : TIJ-24816
Customer No. : 23494

LETTER OF TRANSMITTAL

MAIL STOP ISSUE FEE
Commissioner For Patents
P. O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING 37 CFR §1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450 on the date indicated below.

Lottie Davis
Lottie Davis

09-10-04
DATE

Sir:

The Issue Fee and Publication Fee in the above-identified application are enclosed.

Also enclosed, please find "Letter To The Official Draftsperson" enclosing eight (8) sheets of replacement formal drawings (replacement sheets) in the patent application referenced above.

Please charge any necessary fee to Texas Instruments Incorporated Deposit account No. 20-0668. Texas Instruments Incorporated.

An original and two copies of this sheet are enclosed.

Respectfully submitted,
Texas Instruments Incorporated

By *William B. Kempler*
William B. Kempler
Senior Corporate Patent Counsel
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(972) 917-5452



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
PATENT APPLICATION

In re Application of:

Hiroyuki Enomoto

Serial No.: 09/019,087

Filed: 02/05/1998

Atty. Docket: TIJ-24816

Art Unit: 1765

Examiner: Kunemund, Robert M.

For: MANUFACTURING METHOD OF SEMICONDUCTOR IC DEVICE

Commissioner for Patents

P. O. Box 1450

Alexandria, VA 22313-1450

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LETTER TO THE OFFICIAL DRAFTSPERSON

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Respectfully submitted,



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